

*Unk 3*

producing a first pulse in response to [the] a leading edge of each the received input train of pulses and a second pulse in response to [the] a trailing edge of each one of the received input train of pulses, each one of the first and second pulses having leading edges with the same edge type;

feeding the first and second pulses through a delay line to produce the output train of pulses.

REMARKS

The above-identified patent application has been amended and reconsideration and re-examination are hereby requested.

The claims stand rejected under 35 USC 112, second paragraph. The claims have been amended to more clearly and distinctly point out the subject matter Applicants' regard as their invention.

Claim 1 has been amended to provide antecedent basis for "trailing edge", "first train", and "second train". The claim has also been amended to clarify the "variable delay line". It is respectfully submitted that such claim is now in accordance with 35 USC 112, second paragraph.

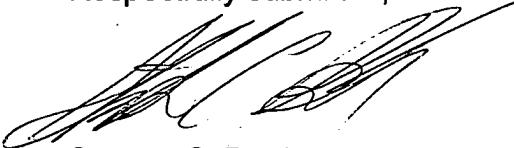
The Examiner's rejection of claim 2 is not understood. Claim 2 points out that the delay-locked-loop provides the control signal for the variable delay line.

Claim 4 has been amended and it is respectfully submitted that such claim is in accordance with 35 USC 112, second paragraph.

The remaining claims have been reviewed and where appropriate amended to conform to the requirements of 35 USC 112, second paragraph.

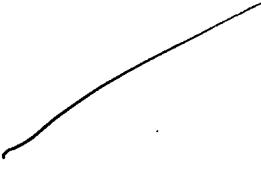
Applicant submits that all of the claims are now in condition for allowance, which action is requested.

Respectfully submitted,



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